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APPLICANTS

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**** CONTINUING DATA *******
 This application is a DIV of 10/211,378 08/05/2002 PAT 6,819,604 *yes LP*

**** FOREIGN APPLICATIONS ******* *yes 10*
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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature <i>[Signature]</i>	Initials <i>[Initials]</i>		

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TITLE
 Semiconductor memory having a defective memory cell relieving circuit

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